

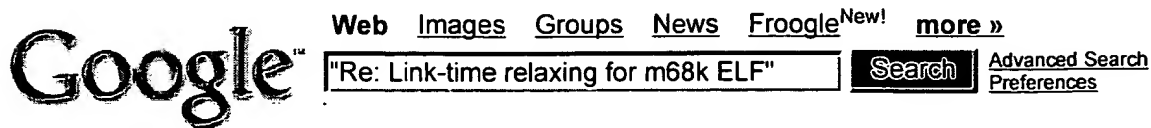
L Number	Hits	Search Text	DB	Time stamp
-	1	("9872475").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:29
-	2	("20020104078").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 17:39
-	0	relaxation adj instruction	USPAT	2004/04/29 20:49
-	14	relaxation adj instruction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 21:08
-	0	elf same link\$3 same instruction same type same jump\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 21:08
-	27	elf same link\$3 same instruction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 20:57
-	67	relocation adj instruction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 21:08
-	18	(relocation adj instruction) same link\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 21:08
-	14	((("5303380") or ("5481706") or ("5519866") or ("5553286") or ("6077315") or ("6219830") or ("6314564")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:30
-	3	object same (relaxation adj instruction)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:52
-	21	(object adj code) same (section adj data) same instruction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:53
-	3	"9928340"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:04
-	3	9928340.0	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:08
-	5	9920905.8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 17:42
-	1	("6021272").PN.	USPAT; US-PGPUB	2004/05/03 18:25

-	1151	((link\$3 adj control\$4 adj language) or LCL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:30
-	0	((link\$3 adj control\$4 adj language) or LCL) same ((relocation or linker) adj operation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:28
-	0	((link\$3 adj control\$4 adj language) or LCL) same ((relocation) adj operation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:28
-	0	((link\$3 adj control\$4 adj language) or LCL) same ((linker) adj operation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:28
-	2	((link\$3 adj control\$4 adj language) or LCL) and ((linker) adj operation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:28
-	4	((link\$3 adj control\$4 adj language) or LCL) and ((relocation or linker) adj operation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:28
-	1151	((linker adj control adj language) or LCL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:30
-	15	((linker adj control adj language))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 18:58
-	17	R adj1 GOTO	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 19:39
-	3	9928340.0	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 20:46
-	430	relocation near4 operation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:57
-	4	(relocation near4 operation) same jump	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 20:52
-	1	("5819097").PN.	USPAT	2004/05/03 20:54
-	1	("6021272").PN.	USPAT	2004/05/03 20:56
-	1	("5946484").PN.	USPAT	2004/05/03 20:56
-	0	(object near3 code) same formar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 20:58

-	1536	(object near3 code) same format	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 20:58
-	310	((object near3 code) same format) same number\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 20:58
-	9	((object near3 code) same format) same number\$3) same (jump\$ or goto)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:04
-	0	object adj code adj format	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:04
-	98	object adj code adj format	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:05
-	16	(object adj code adj format) same link\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:57
-	367195	ELF format	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:57
-	29	ELF adj format	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 21:57
-	2	relax\$5 near3 address\$3 near3 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/05 20:54
-	0	(relax\$5 near3 option) same link\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/05 20:54
-	36	(relax\$5 near3 option)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/05 20:54
-	12	((relax\$5 near3 option)) and link\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/05 20:55
-	0	linker same optimiz\$5 same relax\$5 same relocat4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 10:34
-	8	linker same optimiz\$5 same relax\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:10

-	1	("20040030745").PN.	USPAT;	2004/05/06
-	0	link\$3 same relax\$5 near4 balx	US-PGPUB USPAT;	10:46 2004/05/06
-	0	relax\$5 near4 balx	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	14:10
-	0	link\$3 near4 balx	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:10
-	11	relax near2 option	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:13
-	8058	link\$3 near3 loop\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:13
-	205	linker near3 loop\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:14
-	1	(linker near3 loop\$3) same relocat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:15
-	1	R?IF same R?ENDIF	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:17
-	2	R?GOTO	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 14:29
-	329	loop\$3 same (object adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:51
-	56	loop\$3 near4 (object adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:47
-	193	source same interleaved same object	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:47
-	7	(source same interleaved same object) same link\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:49

-	2	(source same interleaved same object) same loop	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:49
-	12	loop\$3 same (object adj code) same link	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:58
-	565	loop\$3 same (object) same count	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:58
-	41	(loop\$3 same (object) same count) and 717/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 18:07
-	12	ri adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 18:08
-	15	relocation adj instruction near4 module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 19:05
-	1	("6073075").PN.	USPAT	2004/05/06 19:06



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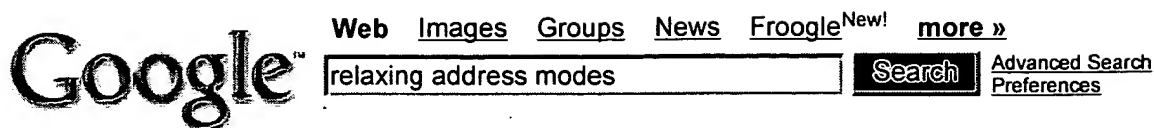
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1 [Link-time optimization of address calculation on a 64-bit architecture](#)

Amitabh Srivastava, David W. Wall

 June 1994 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1994 conference on Programming language design and implementation**, Volume 29 Issue 6
Full text available: [pdf\(1.22 MB\)](#)
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Compilers for new machines with 64-bit addresses must generate code that works when the memory used by the program is large. Procedures and global variables are accessed indirectly via global address tables, and calling conventions include code to establish the addressability of the appropriate tables. In the common case of a program that does not require a lot of memory, all of this can be simplified considerably, with a corresponding reduction in program ...

2 [Compiler techniques for code compaction](#)

Saumya K. Debray, William Evans, Robert Muth, Bjorn De Sutter

 March 2000 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 22 Issue 2
Full text available: [pdf\(409.20 KB\)](#)
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In recent years there has been an increasing trend toward the incorporation of computers into a variety of devices where the amount of memory available is limited. This makes it desirable to try to reduce the size of applications where possible. This article explores the use of compiler techniques to accomplish code compaction to yield smaller executables. The main contribution of this article is to show that careful, aggressive, interprocedural optimization, together with procedural abstr ...

Keywords: code compaction, code compression, code size reduction

3 [Streamlining data cache access with fast address calculation](#)

Todd M. Austin, Dionisios N. Pnvmatikatos, Gurindar S. Sohi

 May 1995 **ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual international symposium on Computer architecture**, Volume 23 Issue 2
Full text available: [pdf\(1.58 MB\)](#)
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For many programs, especially integer codes, untolerated load instruction latencies account